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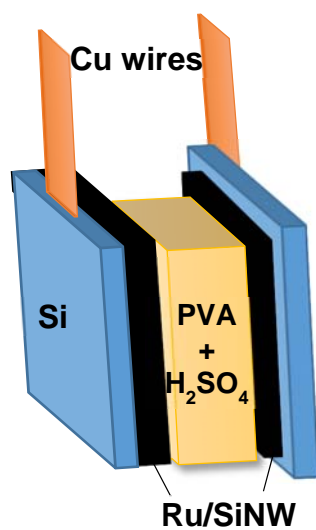
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**Graphical Abstract**

A high performance symmetric solid microsupercapacitor was developed using silicon nanowires coated with Ru nanoparticles.



# High Performance Silicon Nanowires/Ruthenium Nanoparticles Micro-Supercapacitors

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**Abstract**

The continuous increase of small electronic devices calls for small energy storage components, commonly known as micro-supercapacitors, that can ensure autonomous operation of these devices. In this work, we propose a simple and straightforward method to achieve high energy and power densities of a silicon-based micro-supercapacitor, consisting of silicon nanowires decorated with ruthenium nanoparticles (Ru/Si NWs). The Si NWs are obtained through the common vapor-liquid-solid (VLS) growth mechanism, while a simple electroless process is used to deposit Ru nanoparticles. While silicon nanostructuring allows to increase the surface area, coating with Ru NPs introduces a pseudocapacitance necessary to attain high energy and power densities. The Ru/Si NWs micro-supercapacitor exhibits a specific capacitance of  $36.25 \text{ mF cm}^{-2}$  at a current density of  $1 \text{ mA cm}^{-2}$  in a neutral  $\text{Na}_2\text{SO}_4$  electrolyte and a high stability over 25000 cycles under galvanostatic charge-discharge at  $1 \text{ mA cm}^{-2}$ . A solid state supercapacitor is then fabricated with symmetric electrodes separated by a polyvinyl alcohol/sulfuric acid electrolyte. The device displays a specific capacitance of  $\sim 18 \text{ mF cm}^{-2}$  at a current density of  $1 \text{ mA cm}^{-2}$  and a specific power density  $0.5 \text{ mW cm}^{-2}$ . This solid-state nanowire device also exhibits a good stability over 10000 cycles of charge-discharge cycles.

**Keywords:** *Micro-supercapacitors; Silicon nanowires; Ruthenium nanoparticles; Solid-state devices.*

## 1. Introduction

Supercapacitors, also known as ultracapacitors or electrochemical capacitors, have attracted great attention for energy storage owing to their high power density, superior rate capability, rapid charging/discharging rate, long cycle life, and safe operation [1]. Supercapacitors, depending on the charge storage mechanism, can be classified in two types: Electric double layer capacitors (EDLCs) and pseudocapacitors (or redox supercapacitors). While EDLCs store electric charges at the electrode surface-electrolyte interface, in pseudocapacitors, the capacitance arises from rapid redox reaction at the electrode surface [1].

Recent technological progress towards small scale and portable electronic devices calls for the development of miniaturized energy storage sources able to provide these devices with necessary energy to operate autonomously [2, 3]. It is thus desirable to design micro-supercapacitors that can be easily integrated with electronics on the same chip.

Silicon is the basic semiconductor in microelectronics and thus represents an essential electrode material for the fabrication of micro-supercapacitors. The last decades have witnessed a huge body of work devoted to improve the performance of EDLCs using silicon based materials. One important aspect of this work was through the formation of silicon nanostructures to increase the surface area. Initial studies in this area focused on the preparation of silicon nanowires (Si NWs) using the vapor-liquid-solid (VLS) growth mechanism of different morphologies and doping type and level [4-10]. To minimize surface oxidation and achieve stable performance, organic solvents or ionic liquids were used to operate the developed Si NWs-based micro-supercapacitors.

Following this work, another strategy was proposed to ensure stable performance of Si NWs micro-supercapacitors, prepared using VLS or metal-assisted chemical etching (MACE), in aqueous media through coating with silicon carbide [11], diamond [12, 13], organic thin films [14-17], alumina layer [18], TiO<sub>2</sub> [19], and graphene [20]. Even though Si NWs coating enhanced

significantly the stability of the electrode material, it was not possible to enhance the intrinsic capacitance of the electrode using this approach.

With the aim to improve the energy and power densities, recent studies focused on the use of hybrid materials through the introduction of NiO [21], MnO<sub>x</sub> [22] MnO<sub>2</sub> [23], VO<sub>x</sub> [24], and PEDOT [25]. Particularly, transition metal oxides, known as pseudocapacitive materials, are very promising for capacitance increase. The charge storage in these pseudocapacitive materials stems from their redox reactions, allowing rapid and reversible Faradaic surface reactions along with high charge-discharge rate.

Ru-based electrode materials represent an interesting category for designing supercapacitive electrodes owing to their excellent electrochemical properties, including high surface capacitance, rapid reversible redox process, and good cycling stability [26]. Silicon nanowires decorated with ruthenium nanoparticles (Ru/Si NWs) substrates have been reported in a few reports. For example, O'Brien *et al.*[27] prepared Ru/Si NWs by sputtering Ru NPs onto black silicon, obtained using the metal-assisted chemical etching (MaCE) technique, and successfully applied it for gas-phase CO<sub>2</sub> photomethanation. Ru/Si NWs, obtained through electroless deposition of Ru NPs from RuCl<sub>3</sub> precursor on hydrogenated Si NWs, were recently used for electrochemical hydrogen evolution reaction (HER) [28].

To the best of our knowledge, there is only one report on the preparation of ruthenium oxide/Si NWs electrode material for solid-state on chip supercapacitors [29]. In this work, the Si NWs were obtained through metal-assisted anodic etching, while a thin layer of ruthenium oxide was deposited using atomic layer deposition (ALD) at 290°C. A specific capacitance of 19 mFcm<sup>-2</sup> was recorded using cyclic voltammetry in Na<sub>2</sub>SO<sub>4</sub> electrolyte at 5 mV s<sup>-1</sup>. A solid state symmetric supercapacitor based on the modified electrode displayed a specific capacitance of 6.5 mF cm<sup>-2</sup> at 2 mV s<sup>-1</sup> with a good stability over 10000 cycles [29].

In this study, we adopted the electroless deposition technique to decorate Si NWs, produced through the VLS growth mechanism, with Ru nanoparticles at room temperature. This approach is suitable to enhance energy storage capability by taking benefit of the high surface area of Si NWs and the pseudocapacitive properties of Ru nanoparticles. The resulting electrode exhibited a specific capacitance of  $36.25 \text{ mF cm}^{-2}$  and a retention higher than 87% upon 25000 galvanostatic charge-discharge cycles at  $1 \text{ mA/cm}^2$  in aqueous  $\text{Na}_2\text{SO}_4$  electrolyte. The advantage of the fabricated electrode is its high performance and stability. Additionally, the room temperature process used in this work is compatible with the microelectronic fabrication processes (CMOS technology).

## 2. Experimental Part

### 2.1. Materials and Methods

All chemicals and reagents were of analytical grade and used without any purification. Ruthenium chloride hydrate ( $\text{RuCl}_3 \times \text{H}_2\text{O}$ ), polyvinyl alcohol (PVA) powder, sodium sulfate ( $\text{Na}_2\text{SO}_4$ ), hydrogen peroxide ( $\text{H}_2\text{O}_2$ , 30%), hydrofluoric acid (HF, 48%), nitric acid ( $\text{HNO}_3$ ), and sulfuric acid ( $\text{H}_2\text{SO}_4$ ) were obtained from Sigma-Aldrich (France).

The water used throughout the experiments was purified with a Milli-Q system from Millipore Co. (resistivity =  $18 \text{ M}\Omega \cdot \text{cm}$ ).

### 2.2. Preparation of Silicon Nanowires (Si NWs)

Single-side polished silicon (100) oriented n-type wafers (Siltronix, France) (arsenic-doped,  $0.001\text{-}0.005 \text{ Ohm}\cdot\text{cm}^{-1}$  resistivity) were used as substrate. The substrate was degreased in acetone and isopropyl alcohol, rinsed with Milli-Q water, and cleaned in a piranha solution (3:1 concentrated  $\text{H}_2\text{SO}_4/30\% \text{ H}_2\text{O}_2$ ) for 15 min at  $80 \text{ }^\circ\text{C}$  followed by copious rinsing with Milli-Q water. The surface was further dried under a stream of nitrogen.

Silicon nanowires (Si NWs) used in this work were prepared using the vapor-liquid-solid (VLS) mechanism [30]. The fundamental process is based on metal-catalyst-directed chemical vapor deposition of silicon. First, a thin film of gold (4 nm thick) was evaporated on the clean Si substrate. Gold nanoparticles with a wide size distribution were obtained as a result of metal dewetting on the surface. Exposure of the gold-coated surface to silane gas at a pressure of 0.4 T ( $Q=40$  sccm) at 500 °C for 60 min led to Si NWs growth.

**Safety Considerations:** The mixture  $H_2SO_4/H_2O_2$  (piranha) solution is a strong oxidant. It reacts violently with organic materials. It can cause severe skin burns. It must be handled with extreme care in a well-ventilated fume hood while wearing appropriate chemical safety protection. HF is a hazardous acid which can result in serious tissue damage if burns were not appropriately treated.

### 2.3. Preparation of Silicon Nanowires-Ruthenium Nanoparticles (Ru/Si NWs)

The ruthenium-coated Si NWs were obtained by a simple electroless method [31, 32] using ruthenium salt. Briefly, the Si NWs substrate was immersed into an aqueous solution of  $RuCl_3 \times H_2O$  (1 mM)/HF (0.15 M) for different times (30, 45, 60 and 75 min) at room temperature. The resulting interface was rinsed with water and dried under a gentle stream of nitrogen.

### 2.4. Solid State Supercapacitor Fabrication

To prepare the solid PVA/ $H_2SO_4$  electrolyte, 0.5 g of PVA powder was slowly added into 5 mL of deionized water under magnetic stirring to completely dissolve the PVA. 281  $\mu$ L of  $H_2SO_4$  were added into the solution and heated in an oil bath at 85°C until a homogeneous solution was obtained and then cooled down to room temperature.

The polymer-based electrolyte was drop casted on the nanowire electrodes (1 cm  $\times$  0.5 cm) and baked 10 min at 45 °C and kept in air to solidify the electrolyte.



## 2.5. Characterization

### 2.5.1. Scanning Electron Microscopy (SEM)

Scanning electron microscopy (SEM) images were obtained using an electron microscope ULTRA 55 (Zeiss) equipped with a thermal field emission emitter and three different detectors (EsB detector with filter grid, high-efficiency In-lens SE detector, Everhart-Thornley secondary electron detector).

### 2.5.2. X-ray Diffraction (XRD)

X-ray diffraction (XRD) patterns were collected on a high flux Rigaku Smartlab rotated anode, working with a copper K $\alpha$  radiation (1.5418 ang) with an applied voltage of 45 kV and an anode current of 200 mA in the  $2\theta$  range of 25-90°. The beam was made parallel through a multilayer mirror and a reflexion geometry was used. To avoid any saturation of the detector when it passes through the (004) peak of the substrate, the sample was 2° misoriented.

### 2.5.3. Transmission Electron Microscopy (TEM)

TEM analysis was performed on a TECNAT-G2 working at 200 kV, equipped with an EDS spectrometer. The Ru/SiNW composites was dispersed on a thin carbon film of a 3 mm diameter copper grid (See **Fig. S1**).

### 2.4.3. X-ray Photoelectron Spectroscopy (XPS)

X-ray photoelectron spectroscopy (XPS) measurements were performed with an ESCALAB 220 XL spectrometer from Vacuum Generators featuring a monochromatic Al K $\alpha$  X-ray source (1486.6 eV) and a spherical energy analyzer operated in the CAE (constant analyzer energy) mode (CAE=100 eV for survey spectra and CAE=40 eV for high-resolution spectra), using the electromagnetic lens mode. No flood gun source was needed due to the conducting character of the substrates. The angle between the incident X-rays and the analyzer is 58°. The detection angle of the photoelectrons is 30°.

## 2.6. Electrochemical studies

Electrochemical studies were carried out using a potentiostat (Autolab, model 2.01, Methrom, France). Cyclic Voltammetry (CV), and galvanostatic charge-discharge measurements were conducted from -0.6V to +0.2V. Electrochemical Impedance Spectroscopy (EIS) was performed across the frequency range 1 MHz and 0.03 Hz with AC amplitude of 10 mV. All electrochemical tests were performed in 1 M Na<sub>2</sub>SO<sub>4</sub> electrolyte aqueous solution using a typical three-electrode electrochemical cell configuration. Ruthenium-coated SiNWs substrate was used as the working electrode, Pt foil served as the counter electrode, and Ag/AgCl as the reference electrode. All the calculated values were normalized to the surface of the active electrode material (0.29 cm<sup>2</sup>).

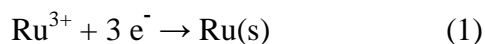
## 3. Results and discussion

The silicon nanowires (Si NWs) investigated in this study were produced by the well-known vapor-liquid-solid (VLS) growth mechanism [30]. In this growth process, Au nanoparticles, obtained through thin Au film (4 nm) dewetting on SiO<sub>2</sub>/Si surface, act as catalytic sites for Si NWs growth. Under controlled conditions, the size of the Si NWs is determined by the size of the Au NPs and the length is defined by the growth time.

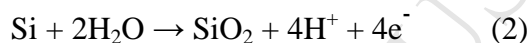
Scanning electron microscopy (SEM) images, depicted in **Fig. 1**, revealed a high density of nanowires with an average diameter in the range of 30-80 nm. The large distribution of the nanowires diameter is due to the size variation of the gold nanoparticles serving as catalysts for Si NWs growth.

Ruthenium nanoparticles (Ru NPs) were deposited on Si NWs through a simple electroless process at room temperature. The Si NWs substrates were first treated with 10% HF aqueous solution to remove the native silicon oxide layer and generate a hydrogen termination. Subsequent immersion in RuCl<sub>3</sub> aqueous solution for various times (30, 45, 60, 75 min) allowed Ru NPs loading on the Si NWs. Electroless deposition of metal nanoparticles on Si NWs and porous

silicon substrates has been studied in many reports in the literature [28, 33-35]. In the electroless mechanism, metal ions with redox potential greater than hydrogen are reduced by electrons from the Si atoms to metal atoms, Eq. 1.



The deposited metal atoms first form nuclei, then nanoclusters and finally nanoparticles. During Ru deposition, the silicon surface is oxidized according to Eq. 2:



The role of HF is to dissolve the oxide layer and generate a fresh hydrogenated Si surface. Indeed, when the electroless process was performed in absence of HF, no particle deposition occurred after 30 min immersion, as evidenced from SEM imaging (data not shown). This is mainly due to electron transfer hindrance by the silicon dioxide barrier on the Si NWs surface.

To assess the influence of deposition time on the specific capacitance, the electroless deposition was performed in  $\text{RuCl}_3$  (1 mM)/HF (0.15 M) aqueous solution at room temperature for various times (30, 45, 60 and 75 min). SEM imaging showed a continuous increase of the nanoparticles density on the Si NWs substrate upon increasing deposition time (**Fig. 2**). Up to 60 min, it is obvious that some of the nanowires are not coated with Ru NPs. A full coverage of the nanowires was attained after 75 min. This sample gave also the best specific capacitance (as discussed later), so additional characterizations were carried out on this sample. Also, the color of the samples turned from light gold (30 min) to dark (75 min), indicating a change of the optical properties of the samples (Insets in **Fig. 2**).

The XRD pattern of the initial Si NWs substrate comprises diffraction peaks at  $2\theta$  of  $28.44^\circ$ ,  $47.22^\circ$  and  $56.11^\circ$  due to (111), (220) and (311) silicon planes, respectively (**Fig. 3**). The peaks at  $38.22^\circ$  and  $44.44^\circ$  are ascribed to Au terminating the nanowires, according to the VLS growth mechanism. The results are in accordance with data reported in the literature [36, 37]. After, loading Ru NPs on the Si NWs *via* electroless deposition, the XRD pattern exhibited additional broad XRD diffraction peaks at  $2\theta$  of  $38.4^\circ$  and  $43.88^\circ$  and other minor peaks, characteristic of Ru, along with the diffraction peaks due to Si and Au. All reflection peaks of the Ru/Si NWs can be easily indexed as pure structure of  $\text{Ru}^0$ , which is in good agreement with the literature data (JCPDS card 70-0274). Though the diffraction peaks of (100) and (101) are overlapped with (002) diffraction due to widening, the product should be the hexagonal phase [38]. According to Scherrer's equation ( $d = k\lambda/\beta \cos \theta$ ), the mean size of particles is about 3-5 nm.

The Ru/Si NWs substrate was further characterized by transmission electron microscopy (TEM). **Figure 4a** depicts a scanning transmission electron microscopy (STEM), in bright field, of a single Si NW decorated with Ru NPs. The diameter of a single Si NW is about 50 nm and the Ru NPs exhibit a small diameter between 3 and 6 nm, in accordance with XRD analysis. STEM characterization allows chemical contrast investigation. Elemental mapping of the Ru/Si NW clearly shows the presence of Si, Ru and O, in accordance with the chemical composition of the material. The presence of oxygen is most likely due to partial oxidation of silicon during exposure to ambient. Indeed, elemental mapping of a single Ru nanoparticle reveals a very small amount of oxygen, ruling out surface oxidation of the Ru NPs (**Fig. S2**).

**Figure 4b** displays the TEM image of the Ru NPs on the silicon surface consisting of small nanocrystallites. The high resolution TEM image in **Figure 4c** exhibits clear fringes for Ru NPs with a lattice spacing of  $2.3 \text{ \AA}$  corresponding to the (100) interplanar distance of hexagonal Ru, in accordance with the XRD analysis.

X-ray photoelectron spectroscopy (XPS) was carried out to examine the chemical composition of the Si NWs substrate before and after loading Ru NPs. **Figure S3** depicts the XPS survey spectrum of Ru/Si NWs. It comprises peaks due to Ru, Si, C and O elements. While Ru and Si are expected to originate from Ru/Si NWs, oxygen may be due to partial oxidation of the silicon core during sample handling and exposure to ambient. Part of the oxygen signal may also arise from surface contamination, as commonly encountered in XPS analysis. The presence of carbon is most likely due to surface contamination during sample handling and storage.

The high resolution XPS spectrum of the Ru<sub>3p</sub> exhibits two symmetric doublets at 461.2 and 483.5 eV assigned to metallic Ru<sub>3p<sub>3/2</sub></sub> and Ru<sub>3p<sub>1/2</sub></sub>, respectively with a spin orbit splitting of 22.3 eV characteristic of Ru in its metallic form [29, 39], **Fig. S4a**. The absence of components at higher binding energies suggests that Ru is mainly in its metallic form, as expected for electroless deposition in RuCl<sub>3</sub>/HF solution. The results are well in accordance with XRD and TEM measurements. The high resolution XPS spectrum of the O<sub>1s</sub> can be deconvoluted into three peaks at 530.7, 531.7 and 533.5 eV ascribed to SiO<sub>2</sub>, O<sub>2</sub><sup>-</sup>/Si-O and OH/adsorbed water, respectively (**Fig. S4b**).

The electrical double layer capacitance of the resulting electrodes was analyzed utilizing cyclic voltammetry measurements in a 3 electrodes configuration consisting of a platinum counter electrode, an Ag/AgCl wire reference electrode and Ru/Si NW working electrode. Electrical connection was made directly to the back side of the sample. Scans were recorded in the potential window of -0.6 to +0.2 V vs. Ag/AgCl electrode using 1 M Na<sub>2</sub>SO<sub>4</sub> aqueous electrolyte. The specific capacitance was calculated from Equation (3):

$$C = \frac{\int j dV}{2 \times V \times r} \quad (3)$$

Where  $j$  is the current density ( $\text{A cm}^{-2}$ ),  $V$  is the potential window,  $r$  is the scan rate, and  $C$  is the specific capacitance; scan rates used for the measurements are 10, 20, 50 and  $100 \text{ mV s}^{-1}$ .

In addition, specific capacitance of the samples was calculated by using the following formula (4):

$$C = j \frac{dt}{dV} \quad (4)$$

The energy density ( $E$ ) and power density ( $P$ ) were determined by using equations (5) and (6), respectively:

$$E = \frac{1}{2} C (dV)^2 \quad (5)$$

and

$$P = \frac{E}{dt} \quad (6)$$

where  $dV$  is the potential range,  $j$  is the current density and  $dt$  is the total time of discharge.

**Figure 5a** depicts the CV of Si NWs substrate recorded before and after decoration with Ru NPs for various deposition times (30, 45, 60 and 75 min) in 1 M  $\text{Na}_2\text{SO}_4$  at a scan rate of  $1 \text{ mA cm}^{-2}$  in the potential window of  $-0.6$  to  $+0.2 \text{ V}$ . The capacitance behavior of the Ru/Si NW electrodes is obvious from the rectangular shape of the CV curves with no apparent contribution from redox processes (Faradaic pseudocapacitance), suggesting that the specific capacitance is dominated by the electrochemical double layer capacitance. This result is in accordance with that recorded for Ru nanoparticles-reduced graphene oxide [40]. The specific capacitance values, deduced from the CV curves using Eq. 1, increased from 12.5 to  $36.25 \text{ mF cm}^{-2}$  upon increasing the deposition time from 30 to 75 min, respectively (**Table 1**). This can be explained by the increase of the Ru NPs density, as observed in SEM analysis.

**Figure 5b** displays the CV curves of Ru/Si NWs (75 min) at different scan rates (5, 10, 30, 50,  $100 \text{ mV s}^{-1}$ ) recorded in 1 M  $\text{Na}_2\text{SO}_4$  aqueous solution at room temperature in the potential window of  $-0.6$  to  $+0.2 \text{ V}$ . The results revealed that the electrodes maintained the rectangular

shape, associated with adsorption-desorption of ions on the electrode surface, even at higher scan rates, indicating a good stability of the electrode material.

The specific capacitance values of Ru/Si NWs (75 min) at different scan rates, calculated from the CV plots, are exhibited in **Fig. 5c** and **Table 2**. It is evident that the specific capacitance decreases upon increasing the scan rate, because of insufficient ion transport and adsorption.

Additionally, the specific capacitance of Ru/Si NWs (75 min) electrode was determined using the galvanostatic charge-discharge curves at different current densities in 1 M Na<sub>2</sub>SO<sub>4</sub> (**Fig. 6**). The specific capacitance values are summarized in **Table 3**. Upon increasing the current density, the specific capacitance value decreases as follows: 112.62, 81.5, 49.37, 36.25, 31.87, 27.5 and 26.25 mF cm<sup>-2</sup> at a current density of 0.1, 0.2, 0.5, 1, 1.5, 2 and 3 mA cm<sup>-2</sup>, respectively.

The stability of an electrode material for supercapacitors is of prime importance to ensure its performance and long-term utilization. **Figure 7** depicts the stability test of the Ru/Si NWs electrode after 25000 cycles. The electrode retains about 80% of its initial specific capacitance after 25000 cycles at 1 mA cm<sup>-2</sup> in the potential window between -0.6 and +0.2 V in 1 M Na<sub>2</sub>SO<sub>4</sub>, indicating a good stability of the Ru/Si NWs electrode. The stability of Ru/Si NWs electrode after 25000 cycles was additionally confirmed using CV (Inset **Fig. 7**) and SEM imaging (**Fig. S5**). Indeed, both the shape of the CV and the surface morphology were not altered significantly after the cycling experiment.

Electrochemical Impedance Spectroscopy (EIS) is a valuable tool to evaluate the electrode-electrolyte interface and understand the electronic transport of the Ru/Si NWs electrode. **Figure 8** exhibits the Nyquist plot within a frequency range from 1 MHz to 0.1 Hz at open circuit potential. The Nyquist plot was fitted using the Randles equivalent circuit (inset **Fig. 8**). In the high frequency region, a semi-circle was observed due to the charge transfer resistance ( $R_{CT}$ ). In the low frequency region, a vertical plot was seen, suggesting a capacitive behavior of the electrode.

The Bod plot in the inset, generated at lower frequency of 100 mHz, allows to determine a phase angle of  $68^\circ$ , which is lower than the value expected for an ideal capacitor ( $90^\circ$ ).

A symmetric solid state supercapacitor was fabricated from the Ru/Si NWs electrode according to the procedure described in the experimental section. **Figure 9a** depicts the CV measurements carried out between -0.4 V and +0.4 V, a potential window where the device displayed rectangular CV curves, at various scan rates ( $5 \text{ mV s}^{-1}$  to  $50 \text{ mV s}^{-1}$ ). The results indicate good electrochemical performance and fast redox reaction of Ru/Si NWs electrode with PVA/H<sub>2</sub>SO<sub>4</sub> gel electrolyte. The specific capacitance values extracted from the CV curves for each scan rate are plotted in **Figure 9b**. A small decrease of the specific capacitance was observed upon increasing the scan rate up to  $50 \text{ mV s}^{-1}$  with a significant decrease at the higher scan rate tested (**Table 4**).

The solid-state symmetric supercapacitor was also tested in galvanostatic charge-discharge experiments at various current densities between 0 V and +0.8 V, as shown in **Figure 9c**. The curves display a triangular shape with linear charge and discharge half cycles, typical of supercapacitors. The energy efficiency can be easily estimated from the capacity ratio between discharge and charge processes and is over 90.45% at  $0.5 \text{ mA cm}^{-2}$  current density. Similarly, the specific capacitance was calculated the galvanostatic curves for different current densities (**Figure 9d**). A similar trend was observed as previously with a continuous decrease of the specific capacitance with increasing the current density (**Table 5**).

The cycle stability of the device fabricated using Ru/Si NWs, obtained after 75 min of electroless deposition, was assessed by continuous charge-discharge measurements for a large number of cycles at a discharge current of  $1 \text{ mA cm}^{-2}$ . According to **Figure 10**, the initial maximum specific capacity value of  $17.5 \text{ mF cm}^{-2}$  only decreased slightly to  $16.88 \text{ mF cm}^{-2}$  after 10000 cycles. The 3.53% decrease relative to the initial capacitance reveals the reliable cyclability



of our symmetric solid state capacitor; the repetitive charge-discharge do not induce noticeable degradation of the hybrid Ruthenium-nanowire silicon array structure.

The high areal capacitances determined for the Ru/Si NW symmetric supercapacitor are also superior to those found for most of other recently reported silicon nanowires-based symmetric micro-supercapacitors [41, 42]: (1.9 mF cm<sup>-2</sup> at 2 mA cm<sup>-2</sup>) for MSC based on diamond-coated Si NW electrodes in ionic liquid Et<sub>3</sub>NH-TFSI electrolyte [25], (14 mF cm<sup>-2</sup> at 0.4 mA cm<sup>-2</sup>) for MnO<sub>2</sub>@SiNWs hybrid symmetric micro-supercapacitors [23] and 2.3 mF cm<sup>-2</sup> at 0.4 mA cm<sup>-2</sup> for Si NW@RuO<sub>x</sub> electrode based solid-state device with ALD deposition [29].

**Figure 11** depicts the Ragone plot i.e. power density (P/mW cm<sup>-2</sup>) vs. energy density (E/mJ cm<sup>-2</sup>) with comparison to other reported values. The electrode material exhibits power and energy densities ranging from 0.5 to 8.5 mWh cm<sup>-2</sup> and from 1.5×10<sup>-2</sup> to 3.5×10<sup>-2</sup> mW cm<sup>-2</sup>, respectively.

## Conclusion

This paper describes a simple method for the fabrication of a symmetric solid-state micro-supercapacitor using silicon nanowire substrates decorated with ruthenium nanoparticles (Ru/Si NWs). The Ru nanoparticles are easily decorated on the Si NWs using electroless deposition technique in a hydrofluoric acid aqueous solution containing Ru ions at room temperature. We have investigated the effect of deposition time on the specific capacitance of the Ru/Si NWs electrode. Under optimized conditions, the Ru/Si NWs displayed a specific capacity as high as 112 mF cm<sup>-2</sup> at an applied current density of 0.1 mA cm<sup>-2</sup> in 1 M Na<sub>2</sub>SO<sub>4</sub>. The performance of a micro-supercapacitor, consisting of two identical Ru/Si NWs electrodes, was investigated electrochemically in 0.5 M H<sub>2</sub>SO<sub>4</sub>/PVA solid electrolyte. A specific capacitance of ~28 mF cm<sup>-2</sup> and a specific power density 0.5 mW cm<sup>-2</sup> were obtained. This solid-state nanowire device also exhibited a good stability over 10000 cycles of charge-discharge cycles. The technique reported in this work is simple and can be implemented for other metal ions to develop various micro-

supercapacitors. Additionally, the process takes place at room temperature which is compatible with microelectronic processing technology.

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**Figure captions**

**Figure 1:** SEM images of the Si NWs at different magnifications.

**Figure 2.** SEM images of Ru/Si NWs for different deposition times in RuCl<sub>3</sub> (1 mM)/HF (0.15 M) aqueous solution at room temperature: 30 min (a), 45 min (b), 60 min (c) and 75 min (d). The insets are the corresponding optical images.

**Figure 3.** X-ray diffraction (XRD) patterns of Si NWs (a) and Ru/Si NWs (b).

**Figure 4. (a)** Scanning transmission electron microscopy (STEM) and elemental mapping, **(b, c)** TEM and high resolution TEM images of Ru NPs.

**Figure 5. (a)** Cyclic voltammetry curves of Ru@SiNW electrode (75 min) recorded at a scan rate of 100 mA cm<sup>-2</sup> in 1 M Na<sub>2</sub>SO<sub>4</sub> aqueous solution for different deposition times, **(b)** C(V) responses at different scan rates (5 to 100 mV s<sup>-1</sup>), **(c)** Effect of scan rate on specific capacitance.

**Figure 6.** Specific capacitance of Ru@SiNW electrode (75 min) obtained from galvanostatic charge-discharge curves recorded at different current densities in 1 M Na<sub>2</sub>SO<sub>4</sub> aqueous solution.

**Figure 7.** Lifetime testing of the device performed using 25 000 complete charge–discharge cycles at a current density of 1 mA cm<sup>-2</sup> between -0.6 and +0.2 V. Inset shows the CV curves of the device before and after the cycling test at a scan rate of 100 V s<sup>-1</sup>.

**Figure 8.** Nyquist plot in the frequency range from 1 MHz to 0.1 Hz. Inset shows the Bode angle phase plot of the electrode.

**Figure 9. (a)** Cyclic voltammetry curves of symmetric solid state supercapacitor, consisting of Ru/Si NWs//PVA-H<sub>2</sub>SO<sub>4</sub>//Ru/Si NWs, recorded at a different scan rates, **(b)** Effect of scan rate on specific capacitance, **(c)** Galvanostatic charge-discharge curves at different current densities, **(d)** Effect of current density on specific capacitance.

**Figure 10.** Lifetime testing of the symmetric solid state supercapacitor device performed using 10 000 complete charge–discharge cycles at a current density of 1 mA cm<sup>-2</sup> between 0 and +0.8 V. The inset is the first corresponding charge-discharge curves.



**Figure 11:** Ragone plot of the symmetric solid state supercapacitor at different current densities and a comparison to other reported Si NWs-based supercapacitors.

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**Table 1.** Specific capacitance of Ru/Si NW electrode for different deposition times obtained from CV curves at  $1 \text{ mA cm}^{-2}$  in  $1 \text{ M Na}_2\text{SO}_4$ .

Deposition time (t/min)	30	45	60	75
IR (V)	0.1	0.05	0.03	0.03
Specific capacitance ( $\text{C/mF cm}^{-2}$ )	12.5	22.66	23.37	36.25

**Table 2.** Specific capacitance values of Ru/Si NW (75 min) electrode obtained from CV curves at different scan rates  $1 \text{ M Na}_2\text{SO}_4$ .

Scan rate ( $\text{Sr/mV s}^{-1}$ )	5	10	30	50	100
Specific capacitance ( $\text{C/mF cm}^{-2}$ )	44.75	37.81	30.50	27.65	23.82

**Table 3.** Galvanostatic charge-discharge plots at different applied current densities for Ru/Si NW in  $1 \text{ M Na}_2\text{SO}_4$ .

Current density ( $\text{j/mA cm}^{-2}$ )	0.1	0.2	0.5	1	1.5	2	3
Specific capacitance ( $\text{C/mF cm}^{-2}$ )	112.62	81.5	49.37	36.25	31.87	27.5	26.25
Specific power ( $\text{mW cm}^{-2}$ )	0.0401	0.0807	0.199	0.386	0.377	0.802	1.292
Specific energy ( $\text{E/mWH cm}^{-2}$ )	0.01	0.0072	0.0043	0.0032	0.0028	0.0024	0.0023

**Table 4.** Specific capacitance values of the symmetric solid-state supercapacitor at different scan rates.

Scan rate ( $\text{Sr/mV s}^{-1}$ )	5	10	30	50	100
Specific capacitance ( $\text{C/mF cm}^{-2}$ )	22.94	22.41	19.34	19.06	14.84

**Table 5.** Specific capacitance values of the symmetric solid-state supercapacitor at different current densities.

Current density (j/mA cm <sup>2</sup> )	0.1	0.2	0.5	1	1.5
Specific capacitance (C/mF cm <sup>2</sup> )	27.75	25.5	21.25	17.5	14.125

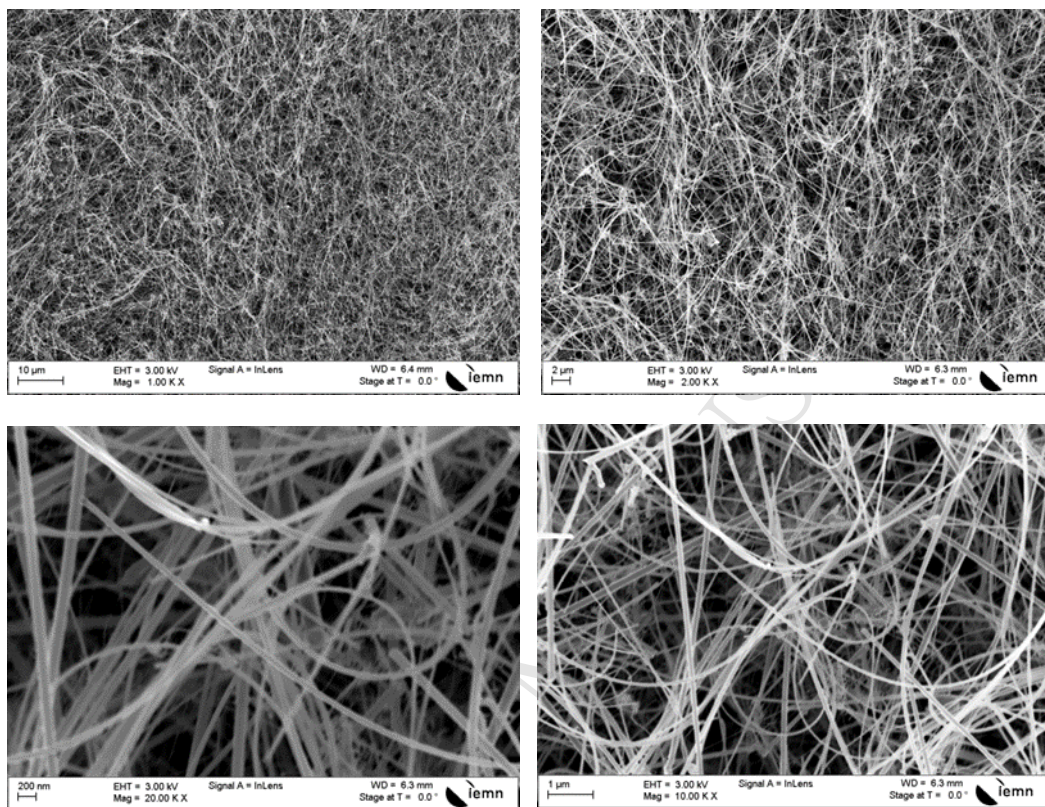


Figure 1

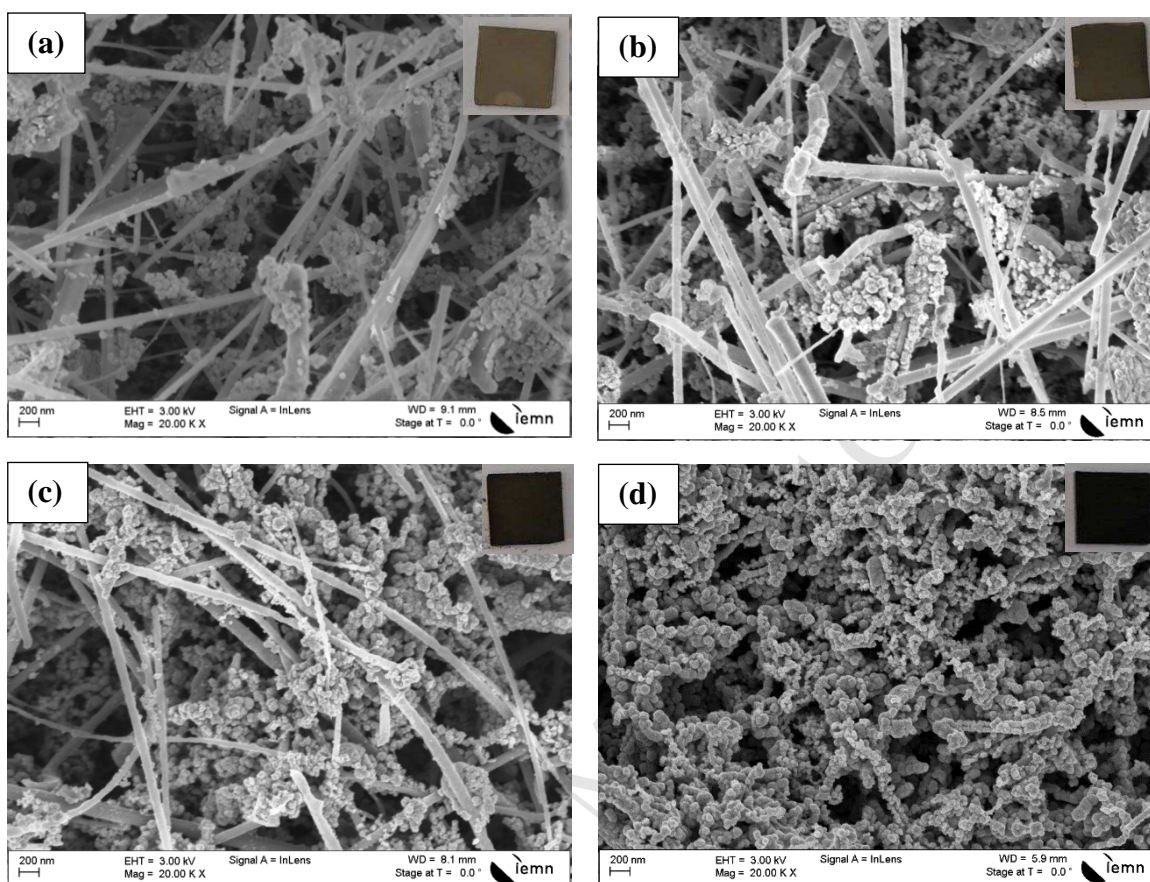


Figure 2

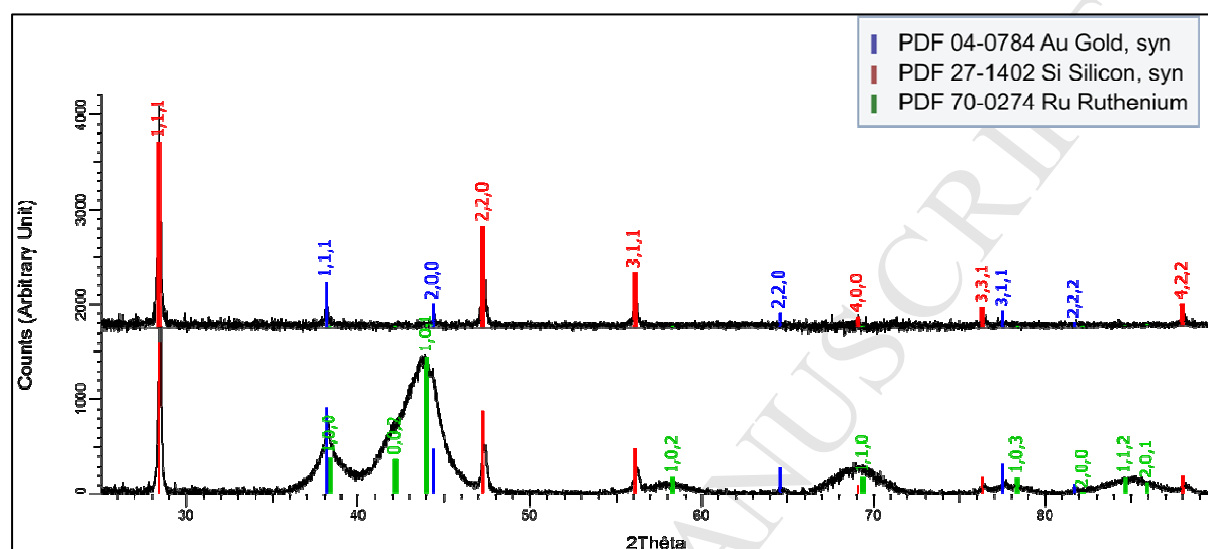
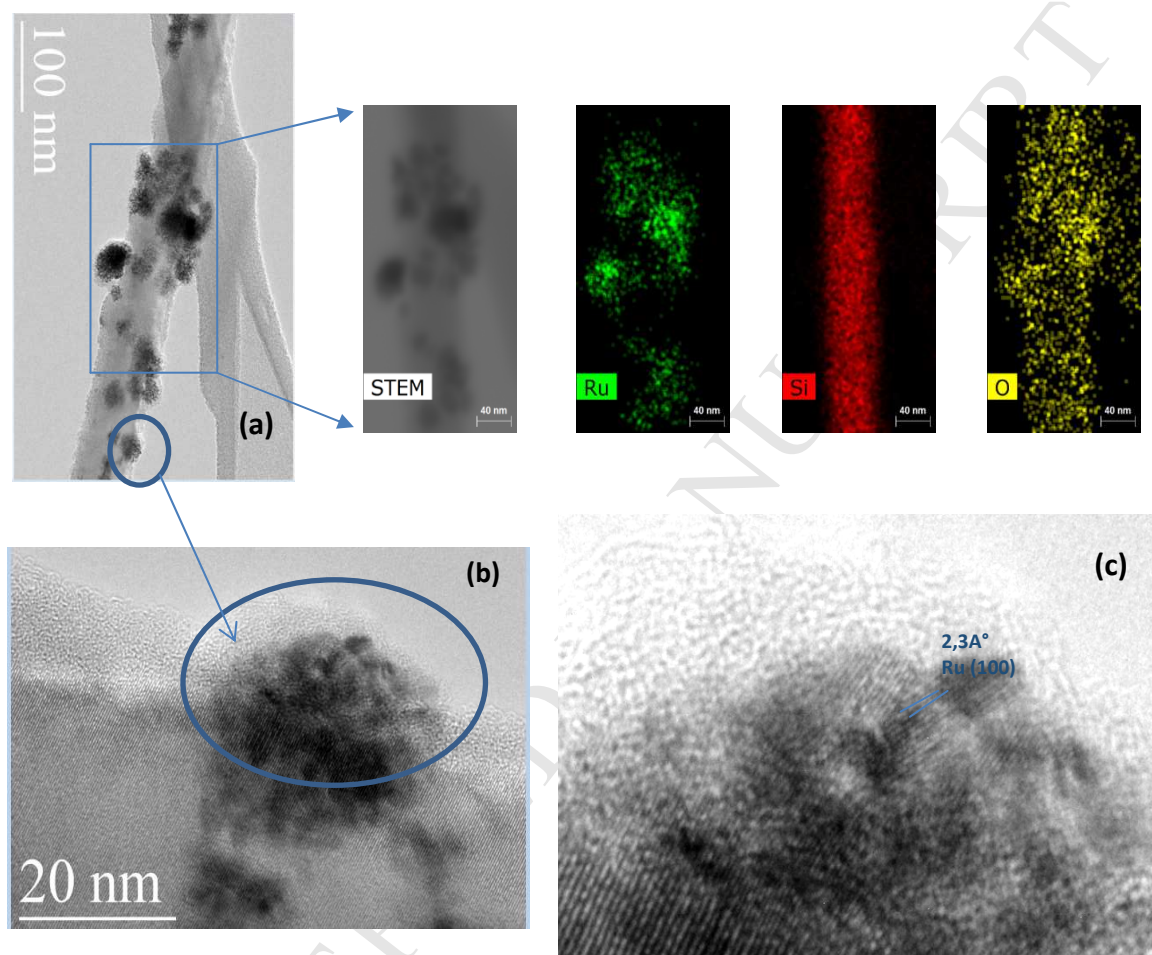


Figure 3



**Figure 4**

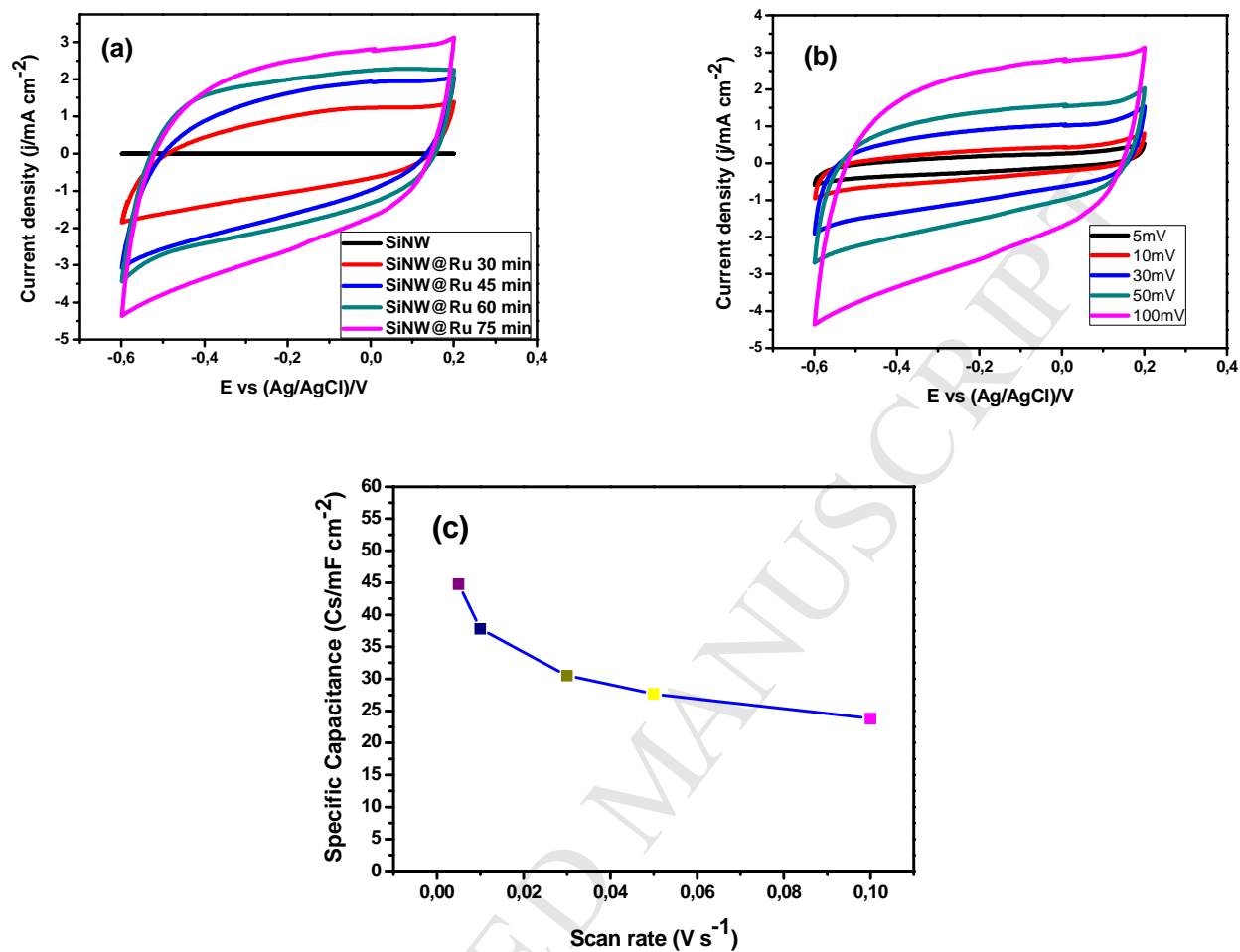


Figure 5



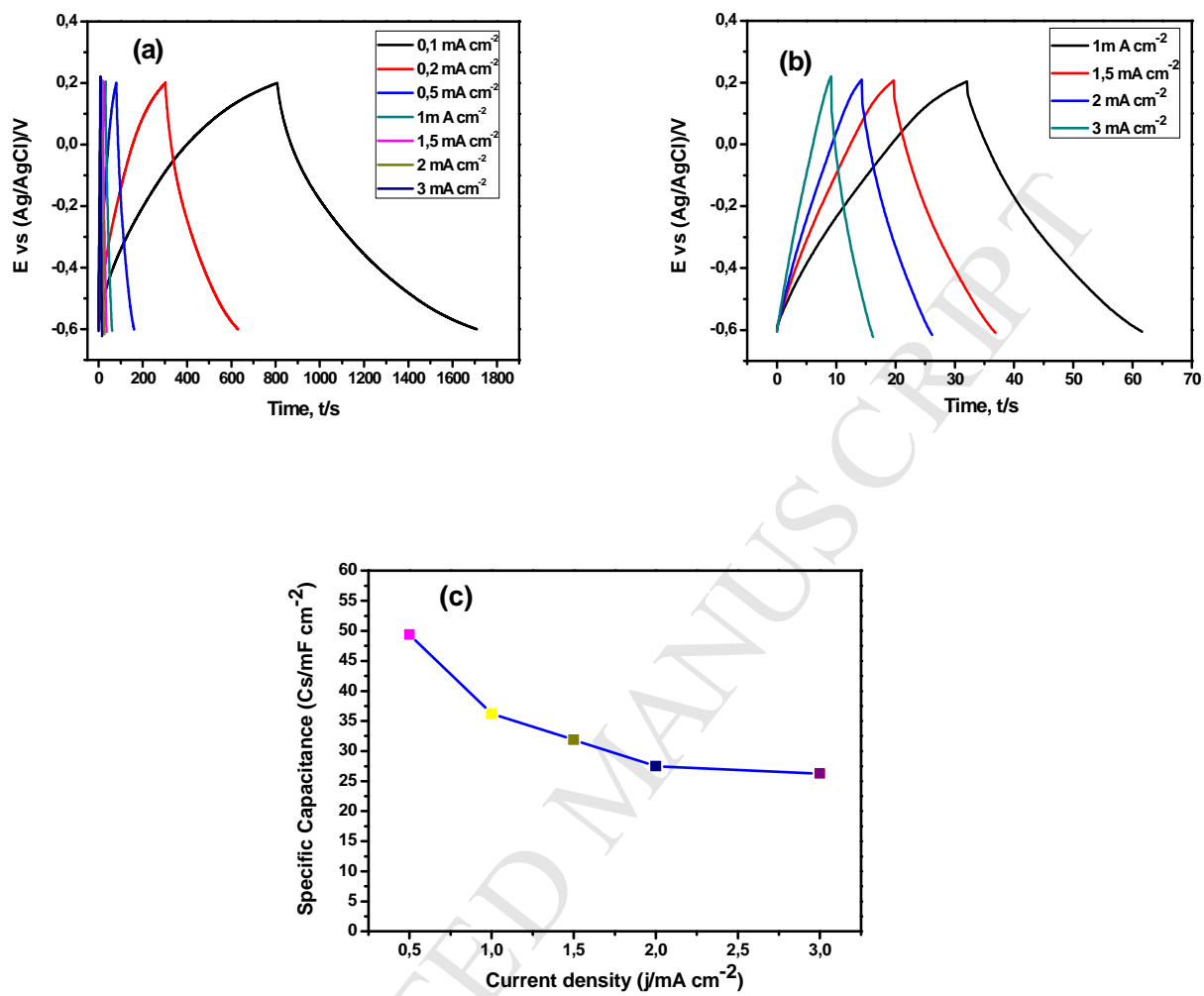


Figure 6

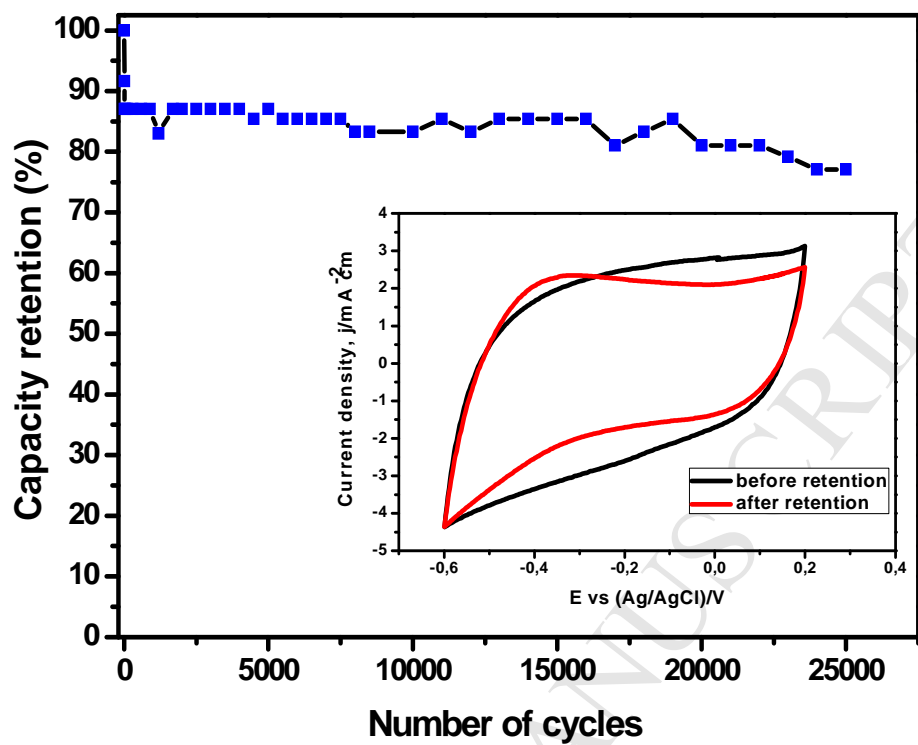


Figure 7

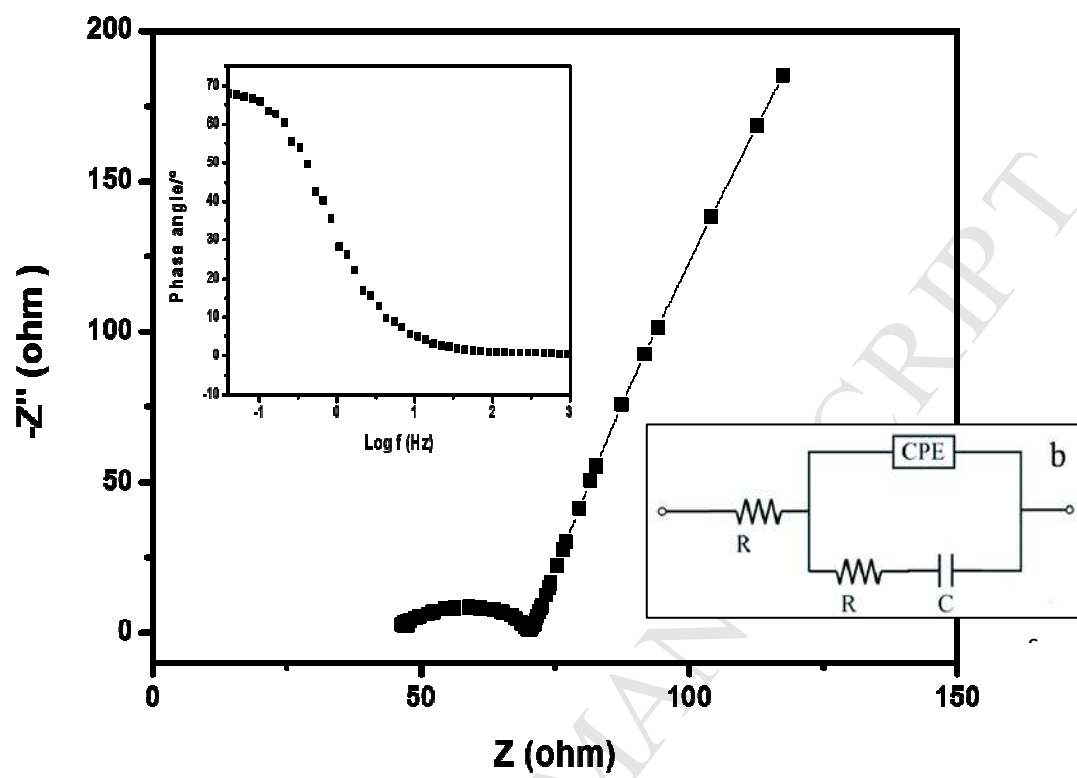


Figure 8

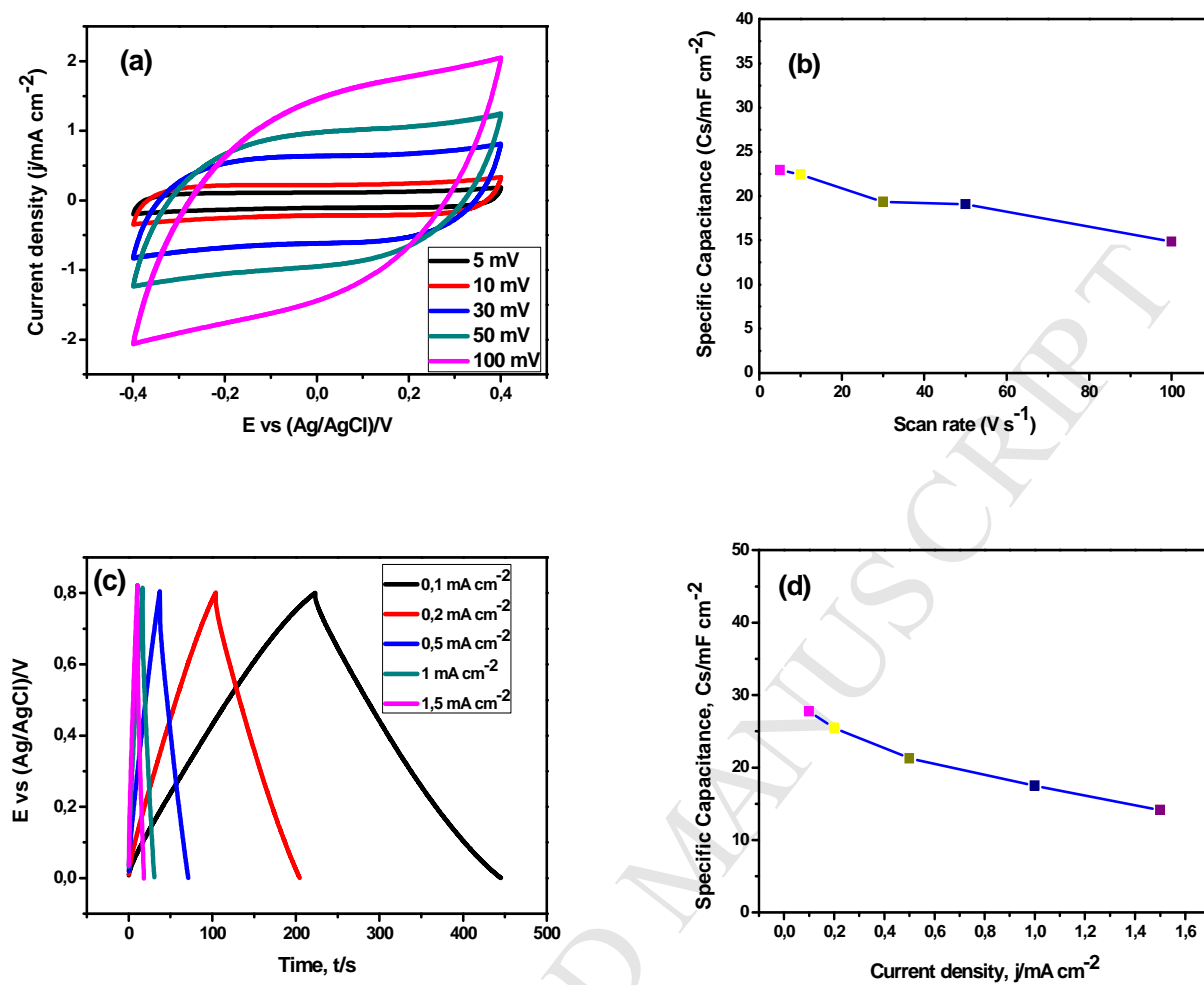


Figure 9

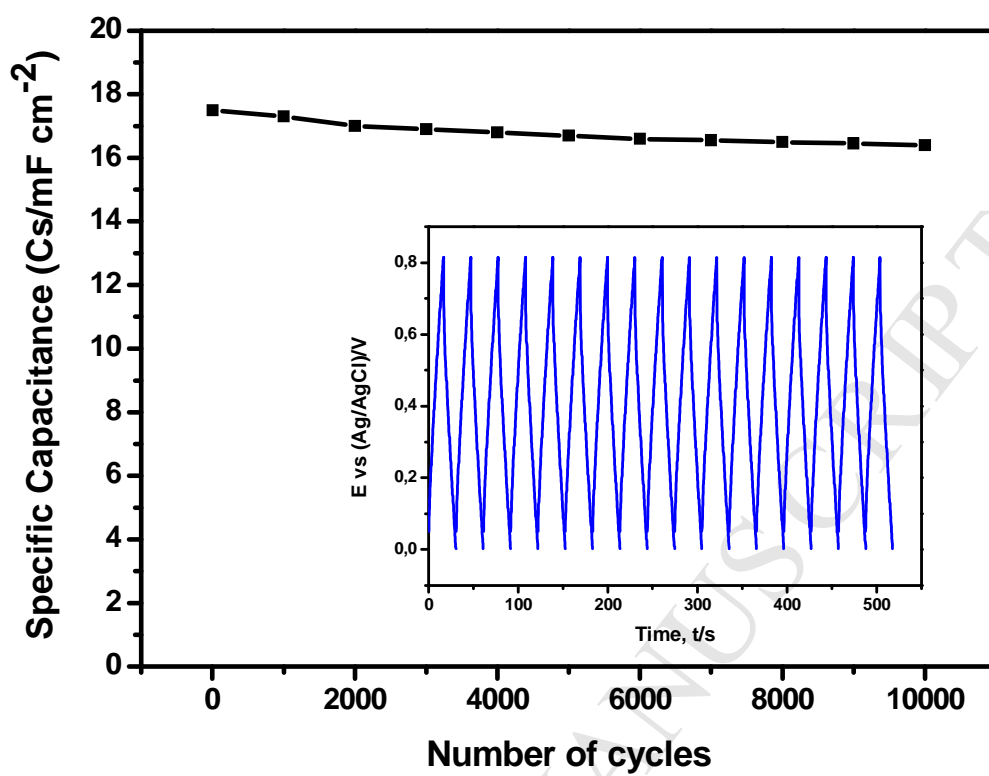


Figure 10

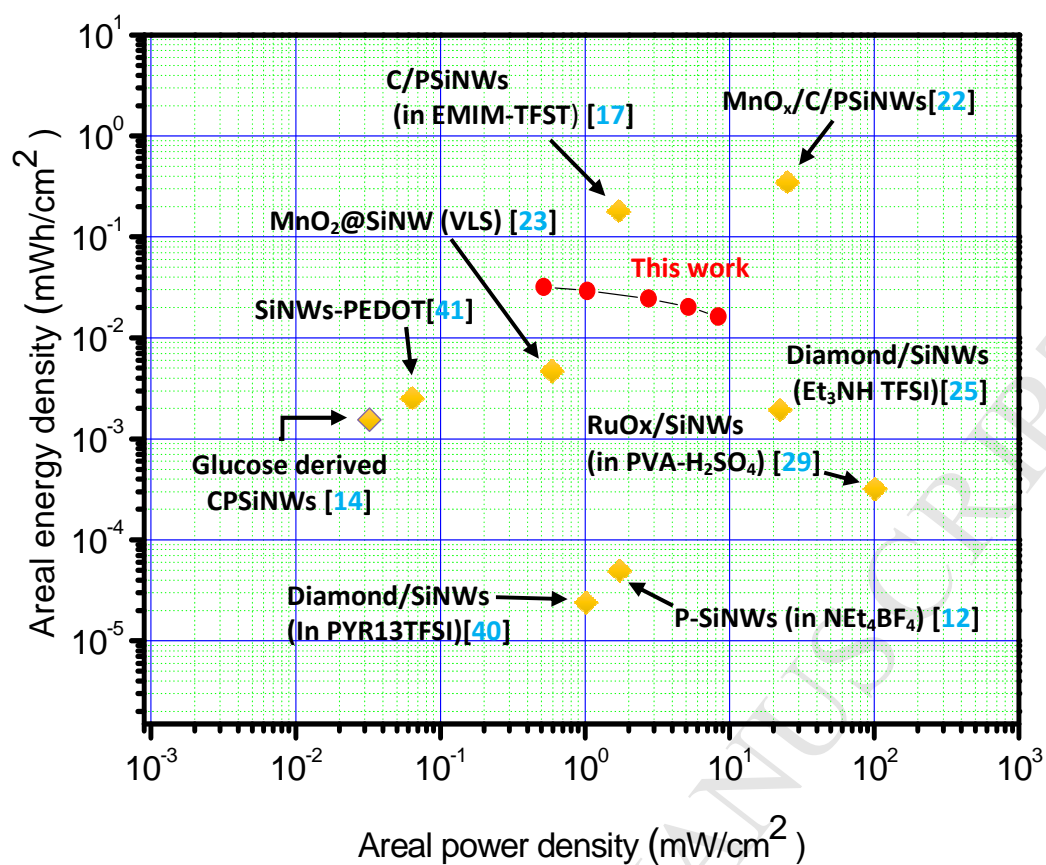


Figure 11

**Highlights**

- Silicon nanowires decorated with ruthenium nanoparticles (Ru/Si NWs) are prepared
- The Ru/Si NWs substrate is used to design a solid-state micro-supercapacitor
- The device displays a specific capacitance of  $18 \text{ mF cm}^{-2}$  at  $1 \text{ mA cm}^{-2}$
- The device exhibits a good stability over 10000 cycles of charge-discharge cycles